Abstract Of The Disclosure

A receiver unit, having at least two signal receivers for analog received signals, an analog-digital converter for converting the analog received signals into digital received signals, and having a signal processing unit for the digital received signals, has at least at least one adder at the output of the signal receivers for adding the analog received signals, the adder being connected with its output to the input of a common analog-digital converter and the digital signal processing unit being configured to separate the added received signals.

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